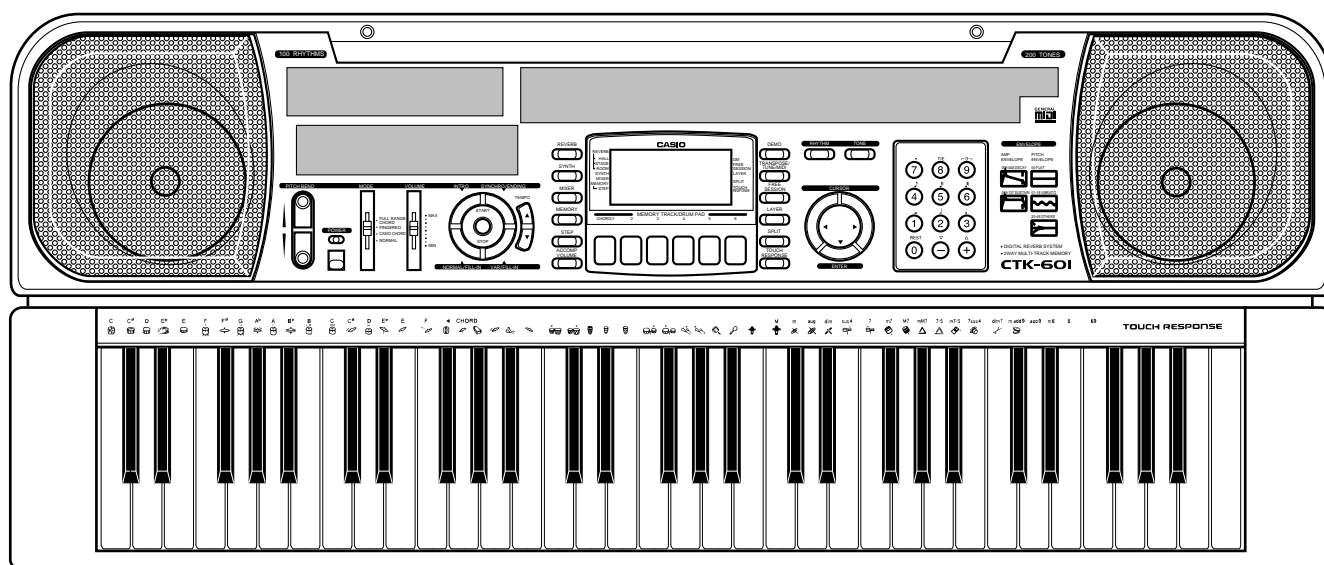


# CASIO®

# Service Manual

(without price)

## CTK-601



CTK-601

**INDEX**

**ELECTRONIC KEYBOARD**

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## SPECIFICATIONS

### GENERAL

Keyboard:	61 standard-size keys, 5 octaves (with touch response on/off)
Drum pads:	6
Tones:	200 (128 General MIDI, 32 synthesized, 8 drum, 32 user); with layer and split
Rhythm instrument tones:	51
Polyphony:	24 notes maximum (12 for certain tones)
Digital effects:	3 reverb types (HALL, STAGE, ROOM)
Auto accompaniment	
Rhythm patterns:	100
Tempo:	Variable (216 steps, ♩ = 40 to 255)
Chords:	3 fingering methods (CASIO CHORD, FINGERED, FULL RANGE CHORD)
Rhythm controller:	START/STOP, INTRO, NORMA/FILL-IN, VAR/FILL-IN, SYNCHRO/ENDING
Accomp volume:	0 to 127 (128 steps)
Free session	
Number of patterns	100 (auto-accompaniment in accordance with selected chord progression)
Memory function	
Songs:	2
Recording tracks:	6 (2 through 6 are melody tracks)
Recording methods:	Real-time, step
Memory capacity:	Approximately 5,200 notes (total for two songs)
Edit function:	Equipped
Demo tunes:	2
Synthesizer function	
Parameters:	PCM set, amp envelope set, attack rate, release rate, pitch envelope set, pitch, level, touch sense, pan
Mixer function	
Channels:	16
Parameters:	Program change number, volume, expression, pan, coarse tuning, fine tuning, on/off/solo
MIDI:	16 multi-timbre receive, GM Level 1 standard
Other functions	
Pitch bend range:	12 semitones upwards and downwards
Transpose:	25 steps (-12 semitones to +12 semitones)
Tuning:	Variable (A4 = approximately 440 Hz ± 50 cents)

## Terminals

MIDI terminals:	IN, OUT
Assignable terminal:	Standard jack (sustain, sostenuto, soft, rhythm start/stop)
Headphone/Output terminal:	Stereo standard jack Output Impedance: 120 $\Omega$ Output Voltage: 4.5 V (RMS) MAX
Power supply terminal:	9 V DC
Power supply	Dual power supply system
Batteries:	Six D-size batteries
Battery life:	Approximately 5 hours continuous operation on manganese batteries
AC adaptor:	AD-5
Auto power off:	Turns power off approximately six minutes after last key operation. Enabled under battery power only, can be disabled manually.
Power consumption:	9 V $\approx$ 7.7 W
Speaker output:	2.5 W + 2.5 W
Dimensions (HWD):	93.1 $\times$ 37.3 $\times$ 12.0 cm (36 11/16 $\times$ 14 11/16 $\times$ 4 3/4 inches)
Weight:	Approximately 5.0 kg (11.0 lbs) (without batteries)

## ELECTRICAL

### Current drain with 9 V DC:

No sound output	200 mA $\pm$ 20 %
Maximum volume	810 mA $\pm$ 20 %
with 12 keys from C3 to B3 pressed in Square wave tone	
Volume: maximum, Touch response: maximum	
Reverb: Hall	

### Phone output level (Vrms with 8 $\Omega$ load each channel):

with key C6 pressed in Bassoon tone	R-ch	100 mV $\pm$ 20 %
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### Speaker output level (Vrms with 4 $\Omega$ load each channel):

with key F5 pressed in Bassoon tone	R-ch	1000 mV $\pm$ 20 %
-------------------------------------	------	--------------------

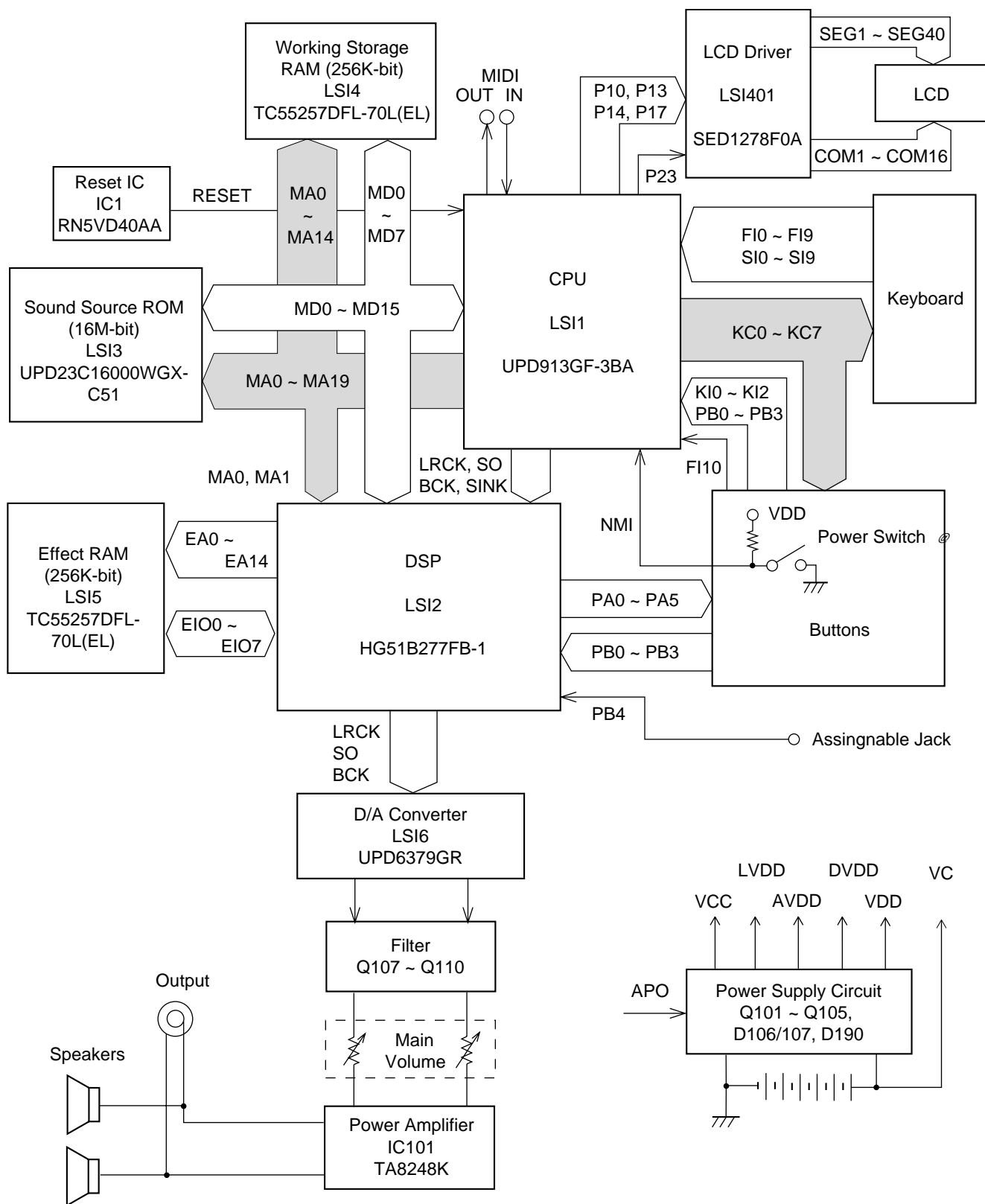
Minimum operating voltage:	6.3 V
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### About General MIDI

General MIDI standardizes MIDI data for all sound source types, regardless of manufacturer. General MIDI specifies such factors as tone numbering, drum sounds, and available MIDI channels for all sound sources. This standard makes it possible for all MIDI equipment to reproduce the same nuances when playing General MIDI data, regardless of the manufacturer of the sound source.

This keyboard supports General MIDI, so it can be used to play commercially available pre-recorded General MIDI data and General MIDI data sent to it from a personal computer.

# BLOCK DIAGRAM



## CIRCUIT DESCRIPTION

### KEY MATRIX

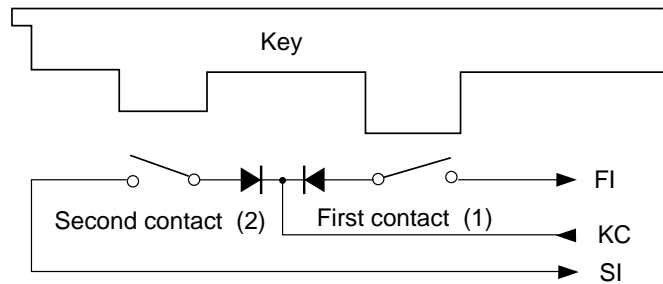
	KC0	KC1	KC2	KC3	KC4	KC5	KC6	KC7
<b>FI0</b>	C2 (1)	C#2 (1)	D2 (1)	D#2 (1)	E2 (1)	F2 (1)	F#2 (1)	G2 (1)
<b>SI0</b>	C2 (2)	C#2 (2)	D2 (2)	D#2 (2)	E2 (2)	F2 (2)	F#2 (2)	G2 (2)
<b>FI1</b>	G#2 (1)	A2 (1)	A#2 (1)	B2 (1)	C3 (1)	C#3 (1)	D3 (1)	D#3 (1)
<b>SI1</b>	G#2 (2)	A2 (2)	A#2 (2)	B2 (2)	C3 (2)	C#3 (2)	D3 (2)	D#3 (2)
<b>FI2</b>	E3 (1)	F3 (1)	F#3 (1)	G3 (1)	G#3 (1)	A3 (1)	A#3 (1)	B3 (1)
<b>SI2</b>	E3 (2)	F3 (2)	F#3 (2)	G3 (2)	G#3 (2)	A3 (2)	A#3 (2)	B3 (2)
<b>FI3</b>	C4 (1)	C#4 (1)	D4 (1)	D#4 (1)	E4 (1)	F4 (1)	F#4 (1)	G4 (1)
<b>SI3</b>	C4 (2)	C#4 (2)	D4 (2)	D#4 (2)	E4 (2)	F4 (2)	F#4 (2)	G4 (2)
<b>FI4</b>	G#4 (1)	A4 (1)	A#4 (1)	B4 (1)	C5 (1)	C#5 (1)	D5 (1)	D#5 (1)
<b>SI4</b>	G#4 (2)	A4 (2)	A#4 (2)	B4 (2)	C5 (2)	C#5 (2)	D5 (2)	D#5 (2)
<b>FI5</b>	E5 (1)	F5 (1)	F#5 (1)	G5 (1)	G#5 (1)	A5 (1)	A#5 (1)	B5 (1)
<b>SI5</b>	E5 (2)	F5 (2)	F#5 (2)	G5 (2)	G#5 (2)	A5 (2)	A#5 (2)	B5 (2)
<b>FI6</b>	C6 (1)	C#6 (1)	D6 (1)	D#6 (1)	E6 (1)	F6 (1)	F#6 (1)	G6 (1)
<b>SI6</b>	C6 (2)	C#6 (2)	D6 (2)	D#6 (2)	E6 (2)	F6 (2)	F#6 (2)	G6 (1)
<b>FI7</b>	G#6 (1)	A6 (1)	A#6 (1)	B6 (1)	C7 (1)			
<b>SI7</b>	G#6 (2)	A6 (2)	A#6 (2)	B6 (2)	C7 (2)			

### BUTTON MATRIX

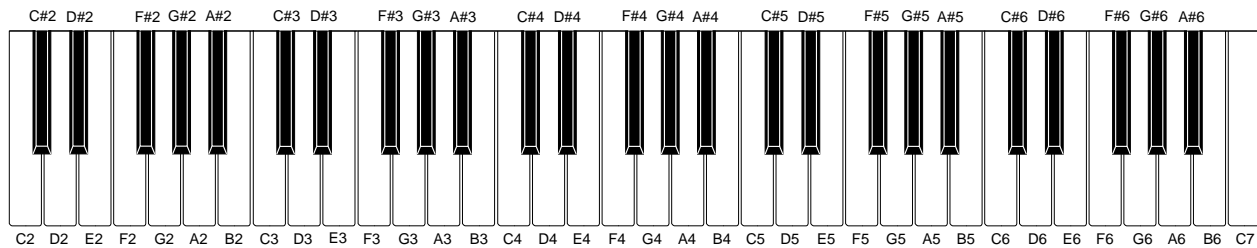
	KC0	KC1	KC2	KC3	KC4	KC5	KC6	KC7
<b>FI10</b>	Drum Pad 3	Drum Pad 1	Pitch Bend ▼	▼ Enter	9	6	+	Tone
<b>KI0</b>	Drum Pad 4	Drum Pad 2	Pitch Bend ▲	Start/ Stop	8	3	—	Rhythm
<b>KI1</b>	Drum Pad 5	Step	Tempo ▼	▶	7	2	0	Demo
<b>KI2</b>	Drum Pad 6	Memory	Tempo ▲	◀	4	5	1	Transpose/ Tune/MIDI

	PA0	PA1	PA2	PA3
<b>PB0</b>	Full Range Chord	Intro	Touch Response	Reverb
<b>PB1</b>	Fingered	Normal/ Fill-In	Free Session	Accomp Volume
<b>PB2</b>	Casio Chord	Synchro/ Ending	Layer	Synth
<b>PB3</b>	Normal	Variation/ Fill-In	Split	Mixer

Note: Each key has two contacts, the first contact (1) and second contact (2).



## NOMENCLATURE OF KEYS



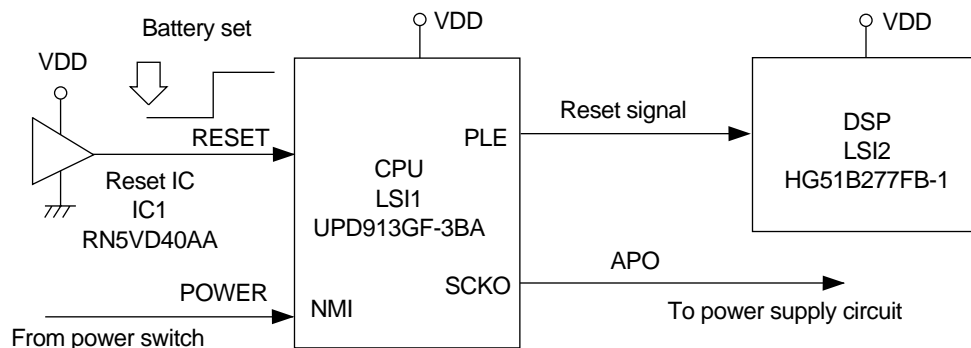
## POWER SUPPLY CIRCUIT

The power supply circuit generates five voltages as shown in the following table. VDD voltage is always generated. The others are controlled by APO signal from the CPU.

Name	Voltage	For operation of
VDD	+5 V	CPU, Reset IC, DSP, Sound source ROM, Working storage RAM, Effect RAM
DVDD	+5 V	LCD driver, Power jack, Sustain jack, MIDI jack
AVDD	+5 V	DAC, Filter
LVDD	+5.6 V	LCD driver
VCC	+9 V	Power amplifier, Pilot lamp

## RESET CIRCUIT

When batteries are set or an AC adapter is connected, the reset IC provides a low pulse to the CPU. The CPU then initializes its internal circuit, and clears the working storage RAM. When the power switch is pressed, the CPU receives a low pulse of POWER signal. The CPU sends APO signal to the power supply circuit, also sends a reset signal to the DSP.



## CPU (LSI1: UPD913GF-3BA)

The 16-bit CPU contains a 1k-byte RAM, three 8-bit I/O ports, two timers, a key controller and serial interfaces. The CPU detects key velocity by counting the time between first-key input signal FI and second-key SI from the keyboard. The CPU reads sound data and velocity data from the sound source ROM in accordance with the selected tone; the CPU can read rhythm data simultaneously when a rhythm pattern is selected. Then the CPU provides 16-bit serial sound data to the DSP. The CPU also controls MIDI input/output and stores sequencer data into the working storage RAM.

The following table shows the pin functions of LSI1.

Pin No.	Terminal	In/Out	Function
1	TXD0	Out	MIDI signal output
2	RXD0	In	MIDI signal input
3	SCK0	Out	APO (Auto Power Off) signal output
4, 5	TXD1, RXD2	In/Out	Data bus for the LCD driver
6	SCK1	Out	1 MHz synchronizing pulse output
7	AVCC	In	DVDD (+5 V) source
8	AN0	In	AC adaptor detection terminal. +5 V when the keyboard is powered by batteries and becomes 0 V to cancel the APO function when AC adaptor is connected.
9	AN1	—	Not used. Connected to ground.
10	AGND	In	Ground (0 V) source
11	BCK	Out	Bit clock output
12	SO	Out	Serial sound data output
13	LRCK	Out	Word clock output
14	GND	In	Ground (0 V) source
15, 16	XLT0, XLT1	In/Out	20 MHz clock input/output
17	VCC	In	+5 V source
18, 19	MD0, MD1	In	Mode selection terminal
20	RSTB	In	Reset signal input
21	NMI	In	Power ON signal input
22	INT/P10	In/Out	Data bus for the LCD driver
23 ~ 30	FI0 ~ FI3 SI0 ~ SI3	In	Terminal for key input signal
31 ~ 38	KC0 ~ KC7	Out	Terminal for key scan signal
39 ~ 46	FI4 ~ FI7 SI4 ~ SI7	In	Terminal for key input signal
47 ~ 50	FI8, FI9 SI8, SI9	—	Not used
51	FI10	In	Terminal for button input signal
52	SI10/P23	Out	Chip enable signal for the LCD driver
53 ~ 55	KI0 ~ KI2	In	Terminal for button input signal
56	MWNB	Out	Write enable signal for the DSP
57 ~ 76	MA0 ~ MA17	Out	Address bus
77	MCSB0	Out	Chip enable signal output for the sound source ROM
78	MCSB1	Out	Not used
79	MCSB2	Out	Chip enable signal output for the DSP

Pin No.	Terminal	In/Out	Function
80	VCC	In	+5 V source
81	GND	In	Ground (0 V) source
82	MRDB	Out	Read enable signal output for the sound source ROM
83 ~ 98	MD0 ~ MD15	In/Out	Data bus
99	PLE	Out	Reset signal output for the DSP
100	P17	In/Out	Data bus for the LCD driver

## DIGITAL SIGNAL PROCESSOR (LSI2: HG51B227FB-1)

The DSP receives 16-bit serial sound data output from the CPU and adds the selected effect to the sound data using the effect RAM. Then the DSP provides the sound data to the DAC. The DSP also controls button input/output.

The following table shows the pin functions of LSI2.

Pin No.	Terminal	In/Out	Function
1 ~ 3, 80	PB0 ~ PB3	In	Button input terminals
4	PB4	In	ASSIGNABLE Jack input
5	SO	Out	Serial sound data output for the DAC
6	WCKO	Out	Word clock output for the DAC
7	VDD3	In	+5 V source
8	TEST	—	Not used
9	RESB	In	Reset signal input
10	VSS2	In	Ground (0 V) source
11, 12	XIN, XOUT	In/Out	20 MHz clock input/output
13	WCKI	In	Word clock input from the CPU
14	SI	In	Serial sound data input from the CPU
15	BCKI	In	Bit clock input from the CPU
16	SINC	In	1 MHz synchronizing pulse input
17	VDD2	In	+5 V source
18 ~ 25	IO0 ~ IO7	In/Out	Data bus
26	RCEB	Out	Chip enable signal output for the working storage RAM
27	VSS3	In	Ground (0 V) source
28	AD1	In	Address bus
29	OEB	Out	Output enable signal for working storage RAM
30	WEB	In	Write enable signal
31	VDD3	In	+5 V source
32	CE2	In	Chip enable signal input. High active.
33	AD0	In	Address bus
34	CE1B	In	Chip enable signal input. Low active.
35 ~ 41, 43	EIO0 ~ EIO7	In/Out	Data bus for the effect RAM
42, 44, 46 ~ 48, 51 ~ 59, 61	EA0 ~ EA12	Out	Address bus for the effect RAM
45	ECEB	Out	Chip enable signal output for the effect RAM



Pin No.	Terminal	In/Out	Function
49	EOEB	Out	Read enable signal output for the effect RAM
50	VSS3	In	Ground(0V) source
60	EWEB	Out	Write enable signal output for the effect RAM
62,66,70,74,78	VSS2	In	Ground source
63,67,71,75,79	VDD2	In	+5 V source
64,65,68,69, 72,73	PA0~PA5	Out	Button scan signal output
76,77	PA6/7	Out	Not used

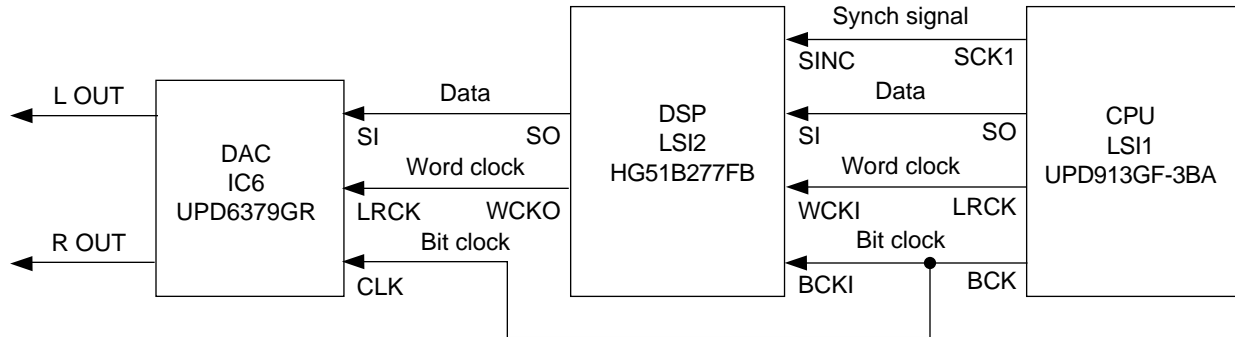
## LCD DRIVER (LSI401:SED1278F0A)

The LCD driver can drive a dot matrix LCD having 40 segment and 16 common lines. The LSI contains 240 graphic symbols in the built-in character generator ROM, and stores 80 characters in the built-in display data RAM. In accordance with command from the CPU, the LSI is capable of displaying up to 16 characters simultaneously. the following table shows the pin functions of LSI 401.

Pin No.	Terminal	In/Out	Function
1 ~ 22, 63 ~ 80	SEG1 ~ SEG40	Out	Segment signal output
23	VSS	—	GND (0 V) source
24, 25	OSC1,OSC2	In/Out	Terminals for the built-in clock pulse generator. The external resistor connected determines the oscillation frequency.
26 ~ 30	V1~V5	In	LCD drive voltage input. Those voltages are used for generating the stepped pulse of the LCD drive signals.
31, 32	LP,XCLS	—	Not used
33	VDD	In	DVDD (+5 V) source
34, 35	FR,DO	—	Not used
36	RS	In	Data/command determination terminal. High: data, Low: command
37	R/W	In	Read/write terminal. High: read, Low: write
38	E	In	Chip enable signal High: enable, the writing is done at fall edge. Low: disable
39 ~ 42	DB0 ~ DB3	—	Not used. Connected to GND (0 V)
43 ~ 46	DB4 ~ DB7	In/Out	Data bus
47 ~ 62	COM1 ~ COM16	Out	Common signal/output

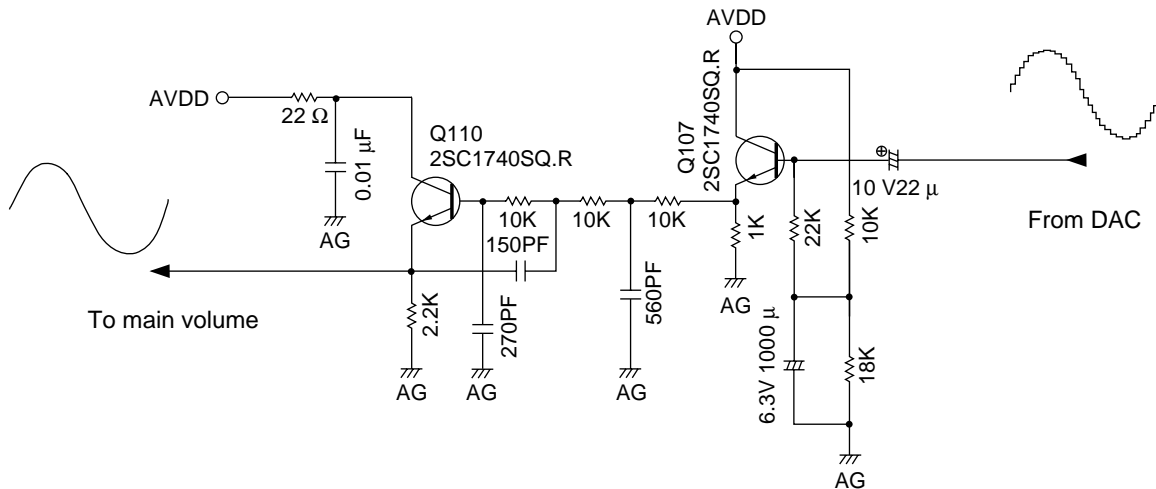
## DAC (LSI6: UPD6379GR)

The DAC receives 16-bit serial data output from the DSP. The data contains digital sound data of the melody, chord, bass, and percussion for the right and left channels. The DAC converts the data into analog waveforms and outputs them to each channel separately.



## FILTER BLOCK

Since the sound signals from the DAC are stepped waveforms, the filter block is added to smooth the waveforms.



## POWER AMPLIFIER (IC101: TA8248K)

The power amplifier is a two-channel amplifier with standby switch.  
The following table shows the pin function of IC101.

Pin No.	Terminal	In/Out	Function
1	NC	—	Not used
2	B.S.2	—	Terminal for a bootstrap capacitor
3	OUT2	Out	Channel 2 output
4	VCC	In	+9 V source
5	OUT1	Out	Channel 1 output
6	B.S.1	—	Terminal for a bootstrap capacitor
7	Power GND	In	Ground (0 V) source
8	Stand by	In	Power control signal input. 0 V: Off, +9 V: On
9	DC	—	Terminal for a decoupling capacitor
10	NF1	In	Negative feedback input
11	IN1	In	Channel 1 input
12	IN2	In	Channel 2 input
13	NF2	In	Negative feedback input
14, 15	Pre GND	In	Ground (0 V) source

# ADJUSTMENT

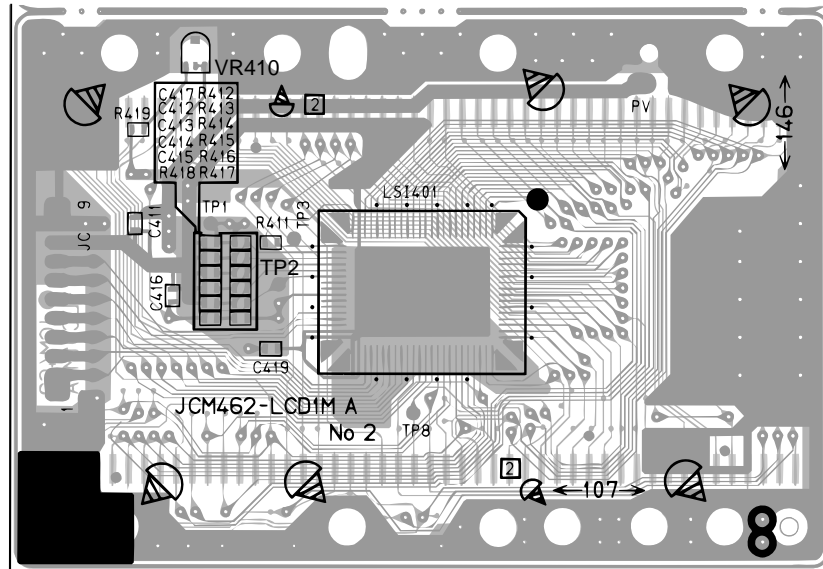
## DISPLAY PCB

1) Items to be adjusted:

Item	Measuring Instrument
Vop voltage setting	Voltmeter

2) Adjustment and Test Point Locations

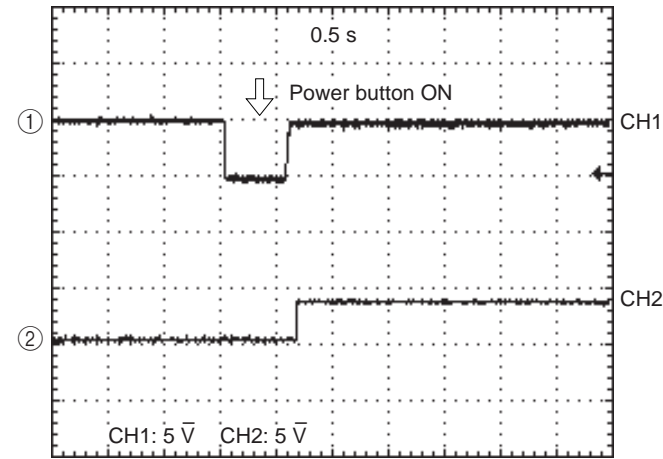
(TOP VIEW)



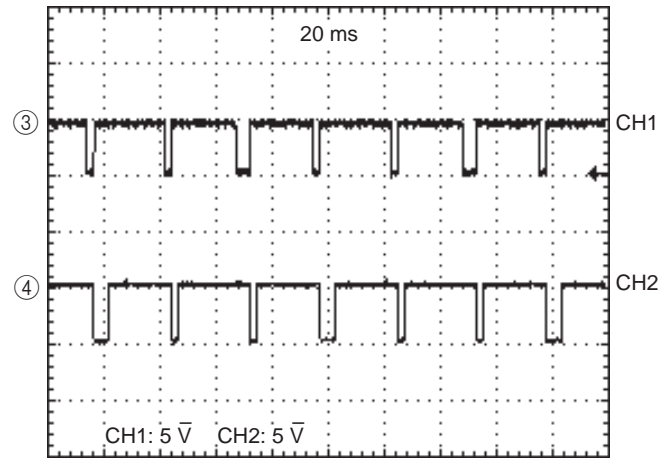
3) Equipment connection/Procedure

Vop voltage setting						
Input Connection	Input Point	Input Signal	Adjust	Output Connection	Output Point	Adjust for
_____	_____	_____	VR410	Voltmeter	TP2	Adjust for $4.4 \pm 0.1$ V reading on voltmeter. Make fine adjustment according to the next instruction.
<p>Watching the LCD at a <math>51.74^\circ</math> angle to the horizontal, adjust Vop voltage so that unenergized segments are seen dimly.</p>						

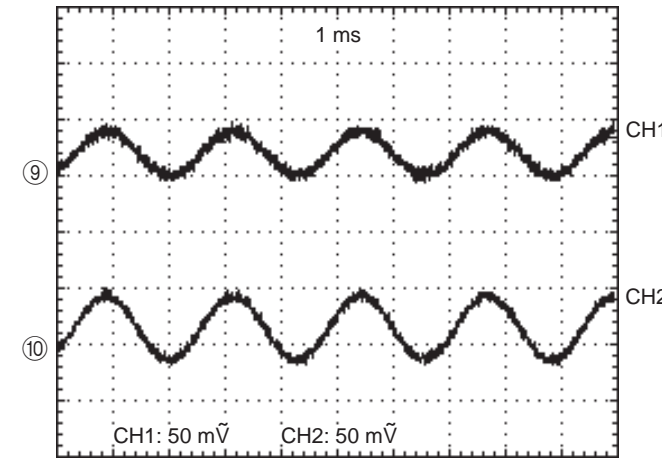
## MAJOR WAVEFORMS



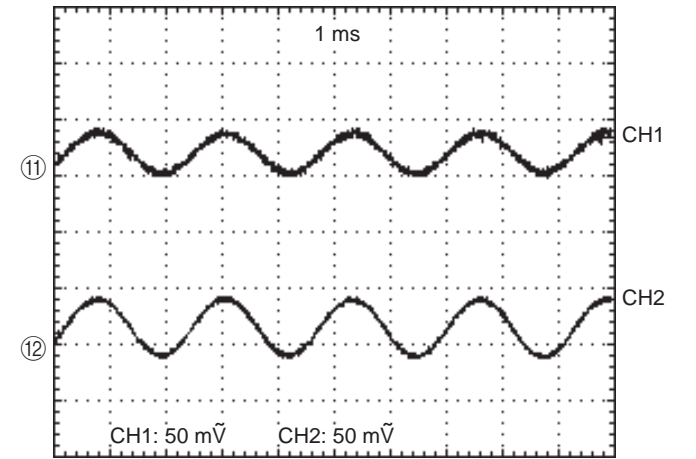
- ① NMI signal  
JE connector pin 16
- ② APO signal  
JD connector pin 9



- ③ Button scan signal PA0  
JD connector pin 14
- ④ Button scan signal PA1  
JE connector pin 14

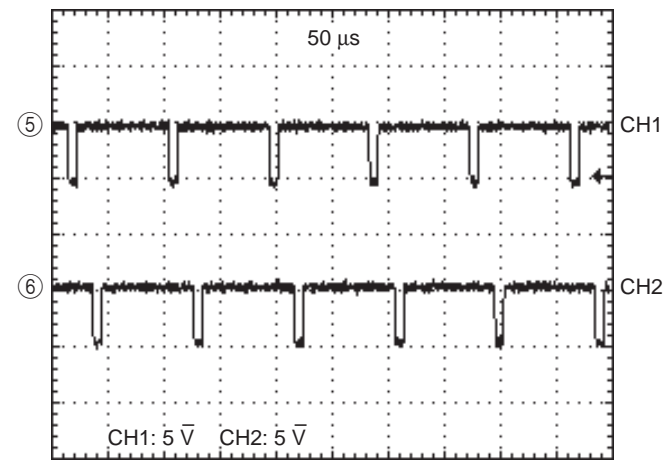


- ⑨ DAC output (L-ch)  
JD connector pin 6
- ⑩ Dac output (R-ch)  
JD connector pin 7

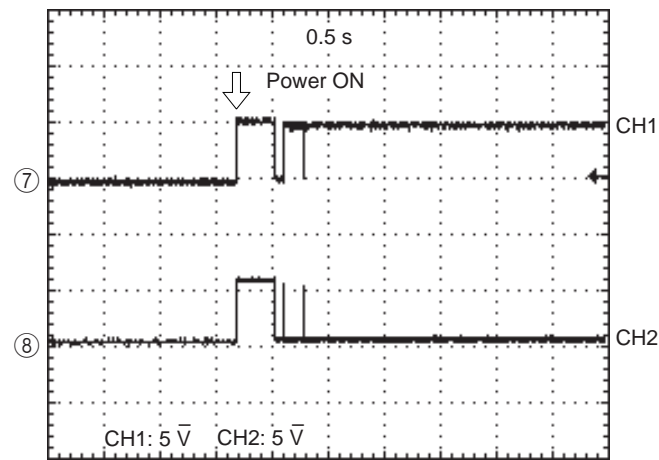


- ⑪ Sound signal (L-ch)  
JF connector pin 2
- ⑫ Sound signal (R-ch)  
JF connector pin 1

Tone : Whistle (078)  
 Key : A4  
 Touch response : Off  
 Reverb : Off  
 Volume : Maximum



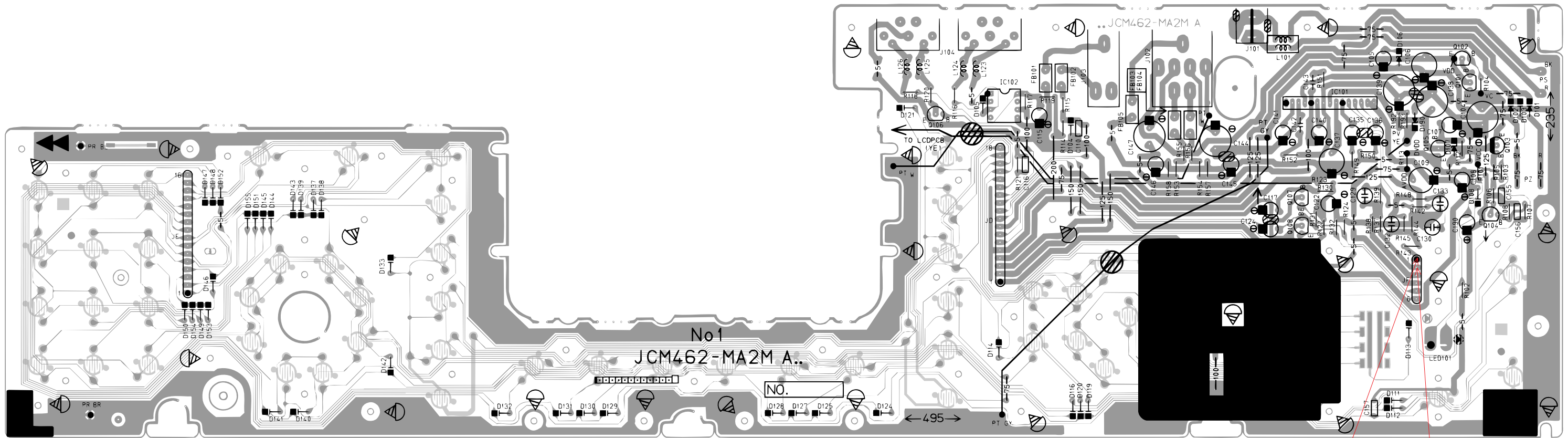
- ⑤ Key scan signal KC0  
JB connector pin 1
- ⑥ Key scan signal KC1  
JB connector pin 2



- ⑦ RS signal PA4  
JC connector pin 7
- ⑧ R/W signal PA5  
JC connector pin 6

# PRINTED CIRCUIT BOARDS

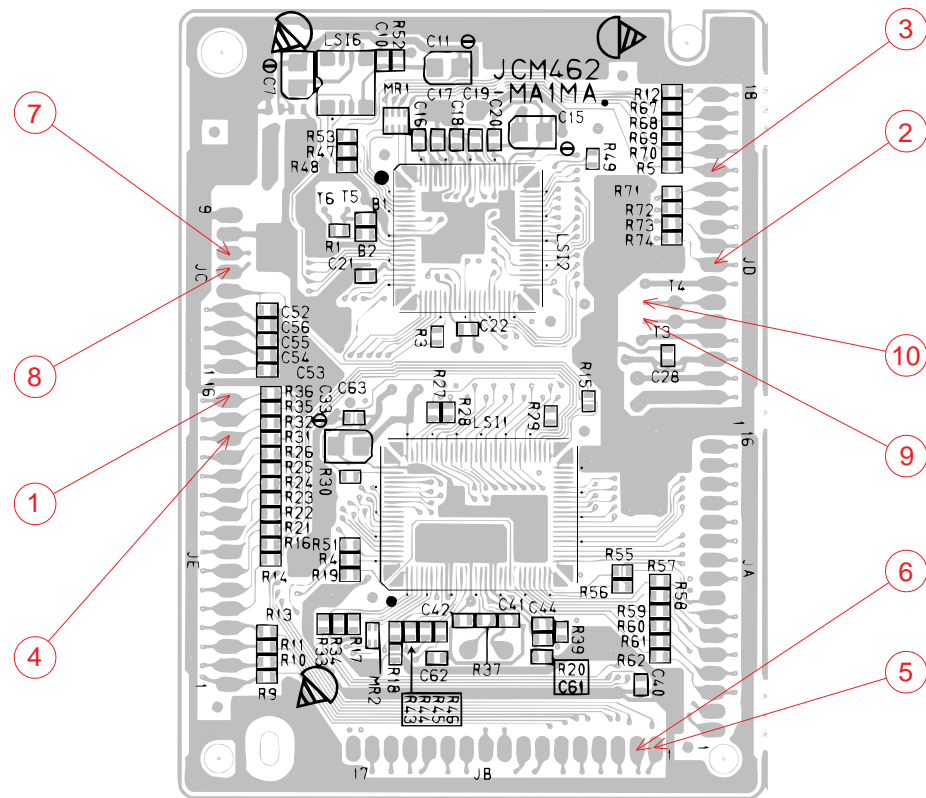
Sub PCB JCM462-MA2M



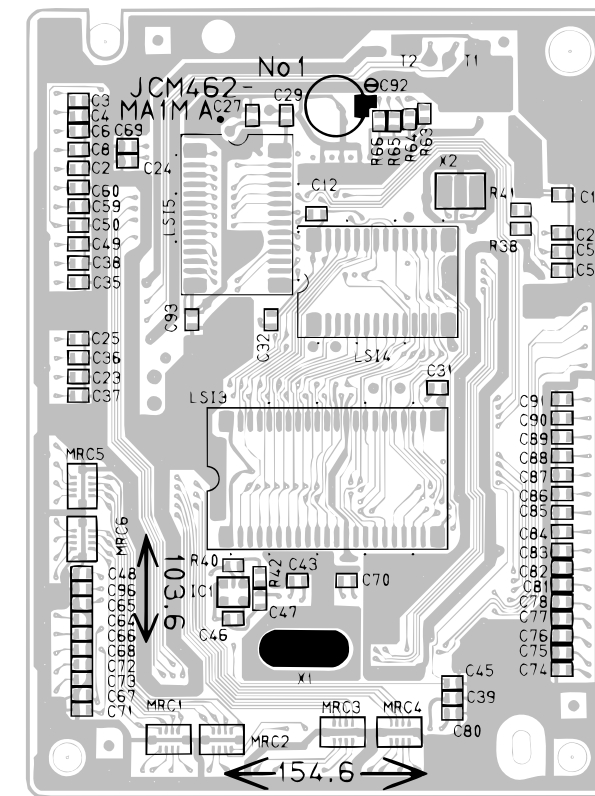
Top View

12 11

Main PCB JCM462-MA1M



Top View

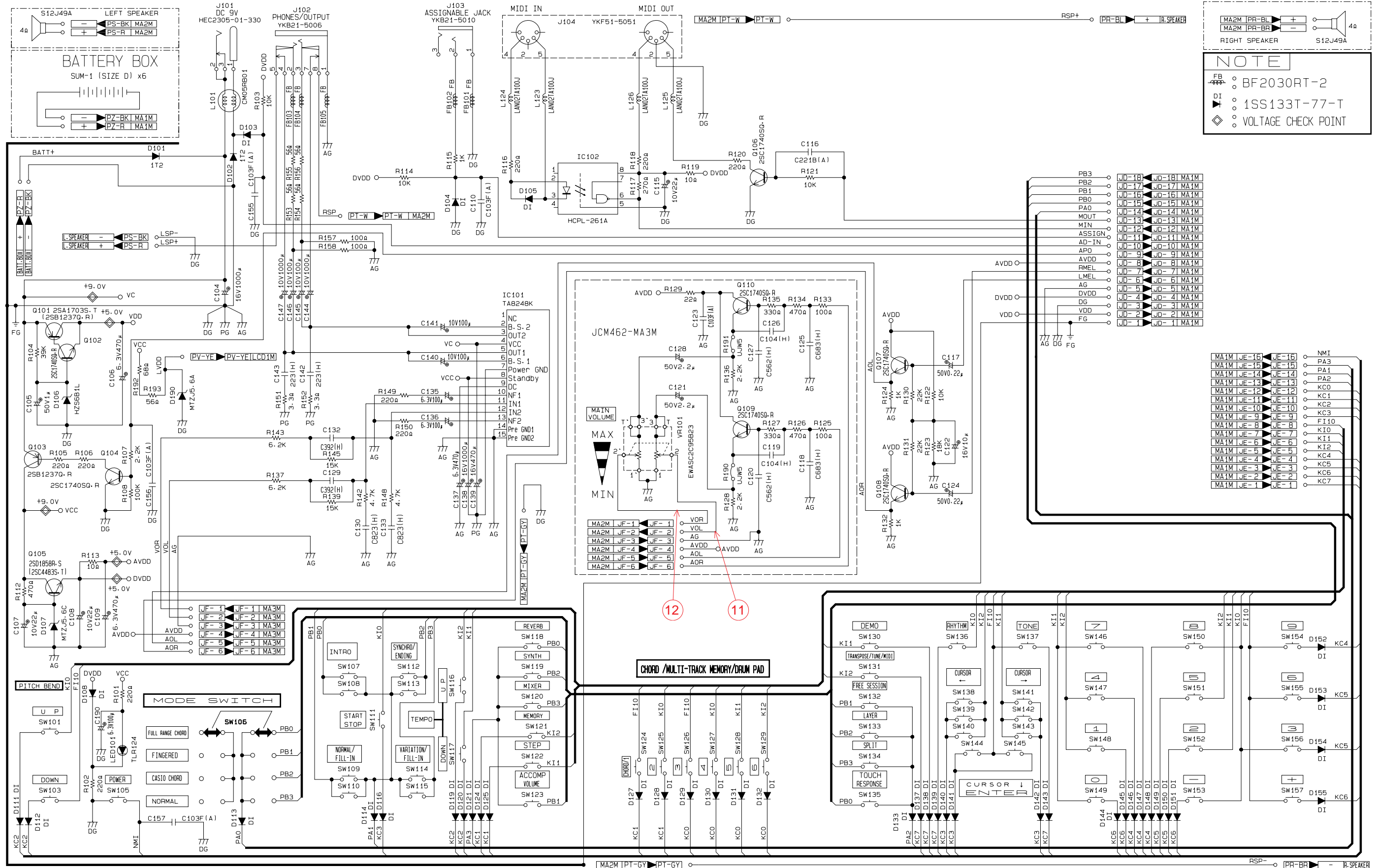


Bottom View





Sub PCB JCM462-MA2M/Volume PCB JCM462-MA3M



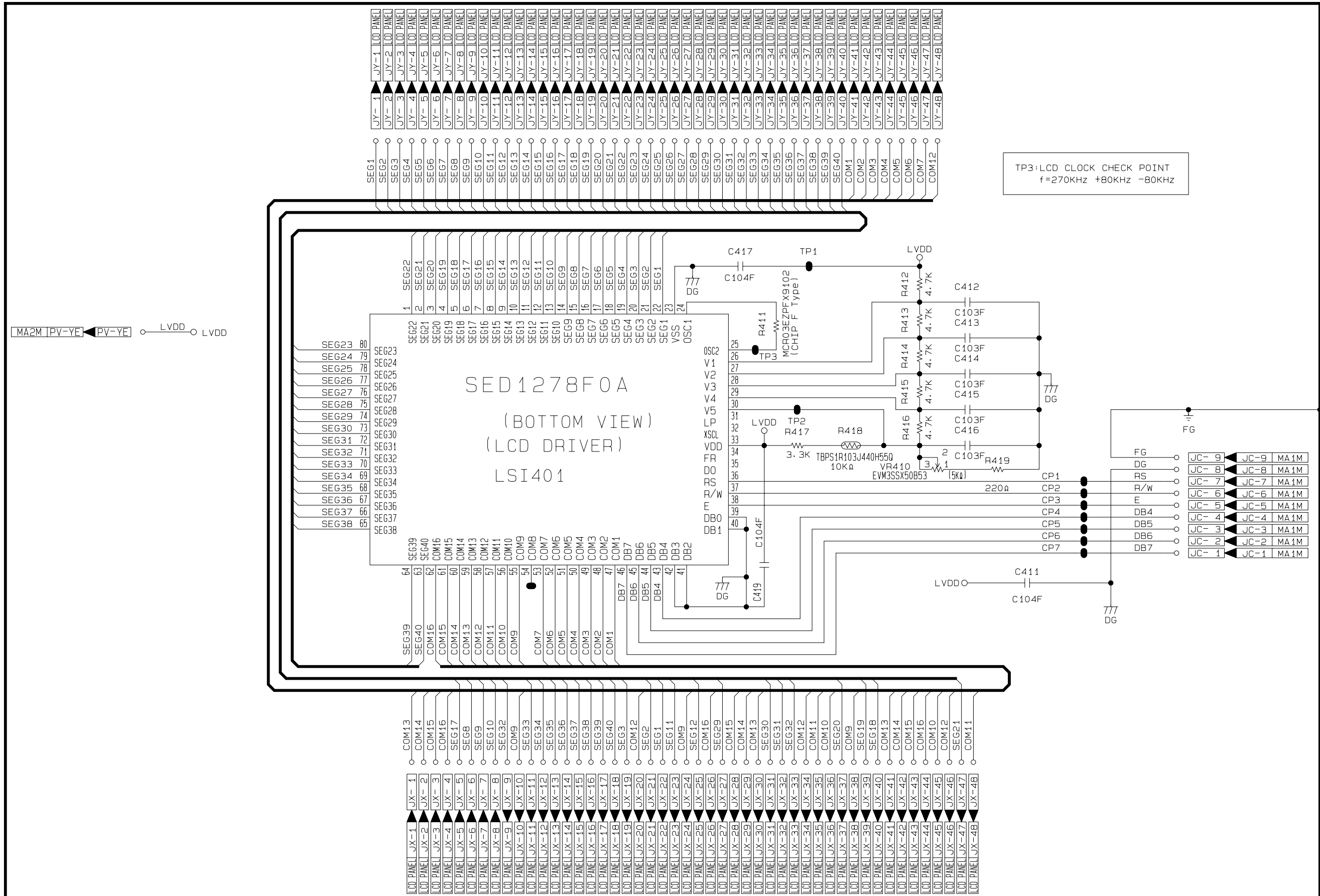
**NOTE**

- FB : BF2030RT-2
- DI : 1SS133T-77-T
- ◇ : VOLTAGE CHECK POINT

MA1M JF-16	JF-16	NMI
MA1M JF-15	JF-15	PA3
MA1M JF-14	JF-14	PA1
MA1M JF-13	JF-13	PA2
MA1M JF-12	JF-12	KC0
MA1M JF-11	JF-11	KC1
MA1M JF-10	JF-10	KC2
MA1M JF-9	JF-9	KC3
MA1M JF-8	JF-8	F10
MA1M JF-7	JF-7	K10
MA1M JF-6	JF-6	K11
MA1M JF-5	JF-5	K12
MA1M JF-4	JF-4	KC4
MA1M JF-3	JF-3	KC5
MA1M JF-2	JF-2	KC6
MA1M JF-1	JF-1	KC7

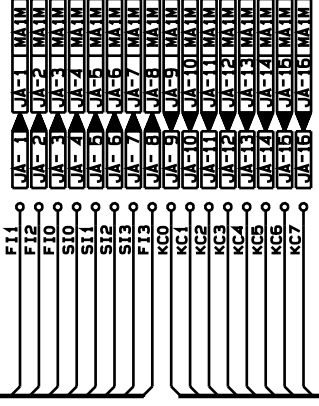
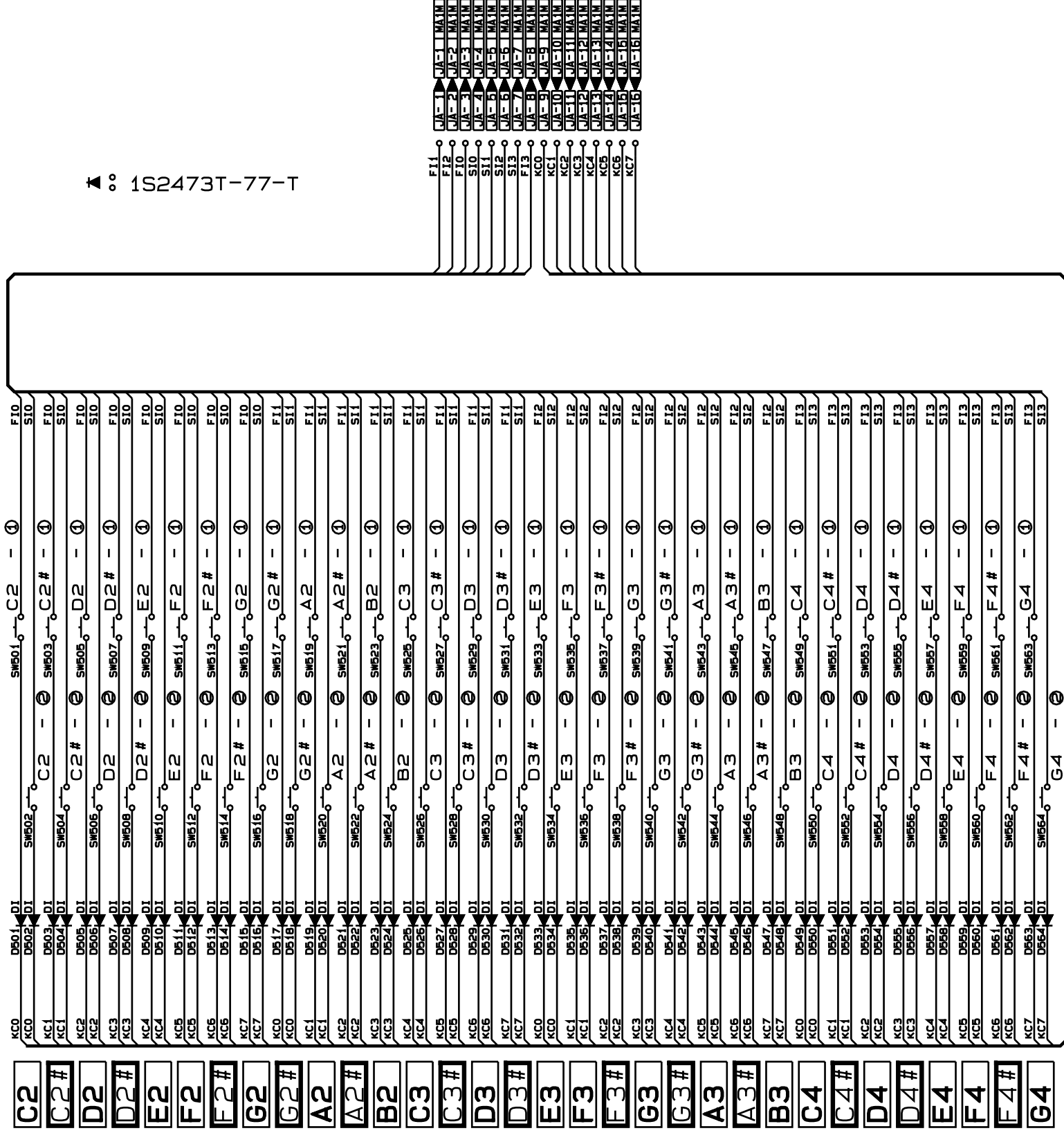


Display PCB JCM462-LCD1M

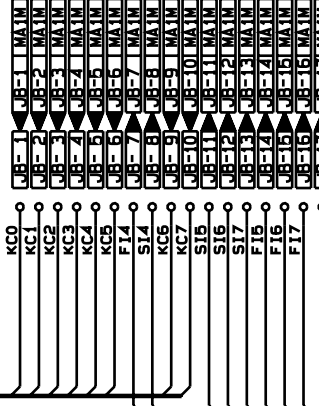
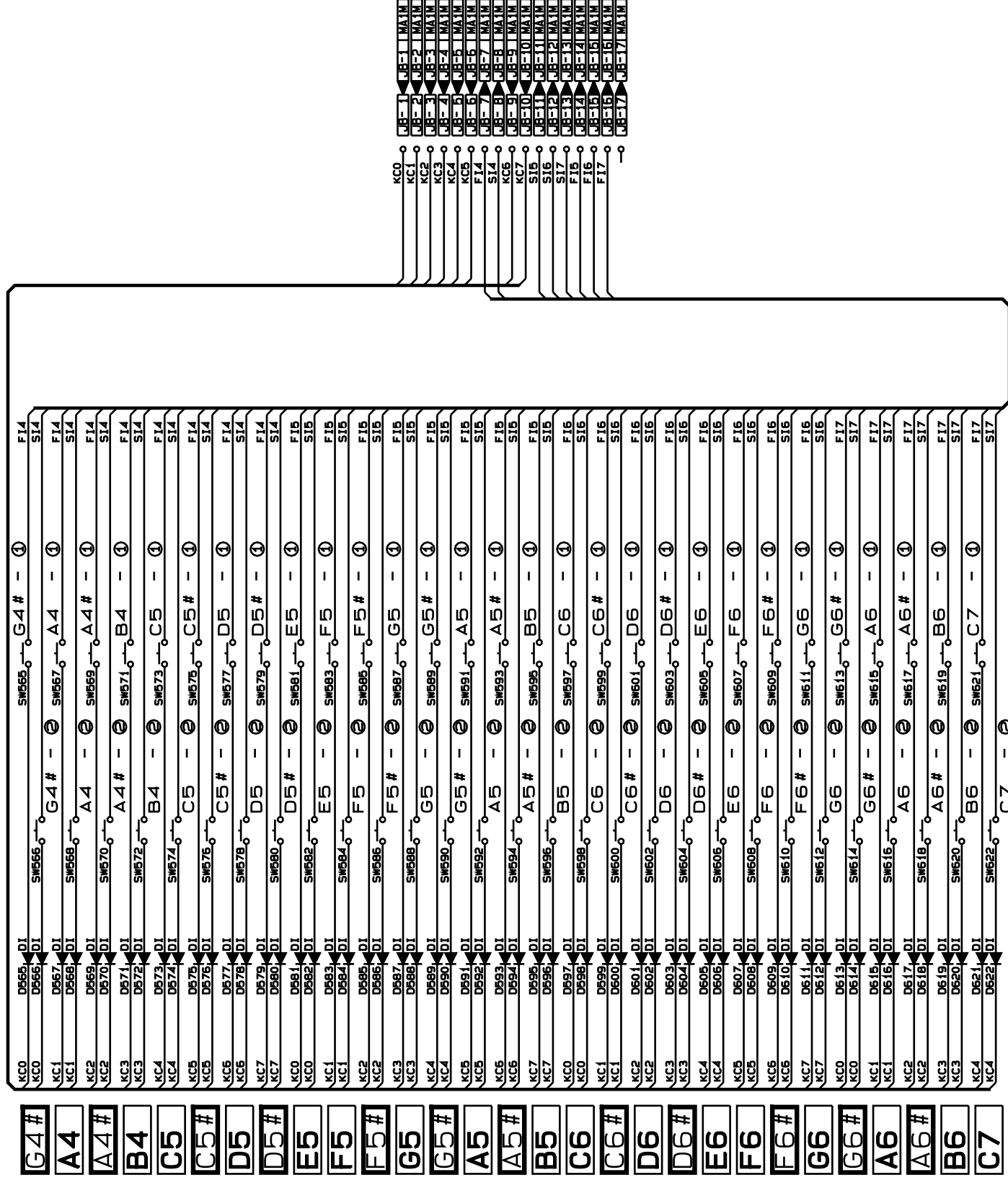


# JCM617T-KY1M

▲ : 1S2473T-77-T

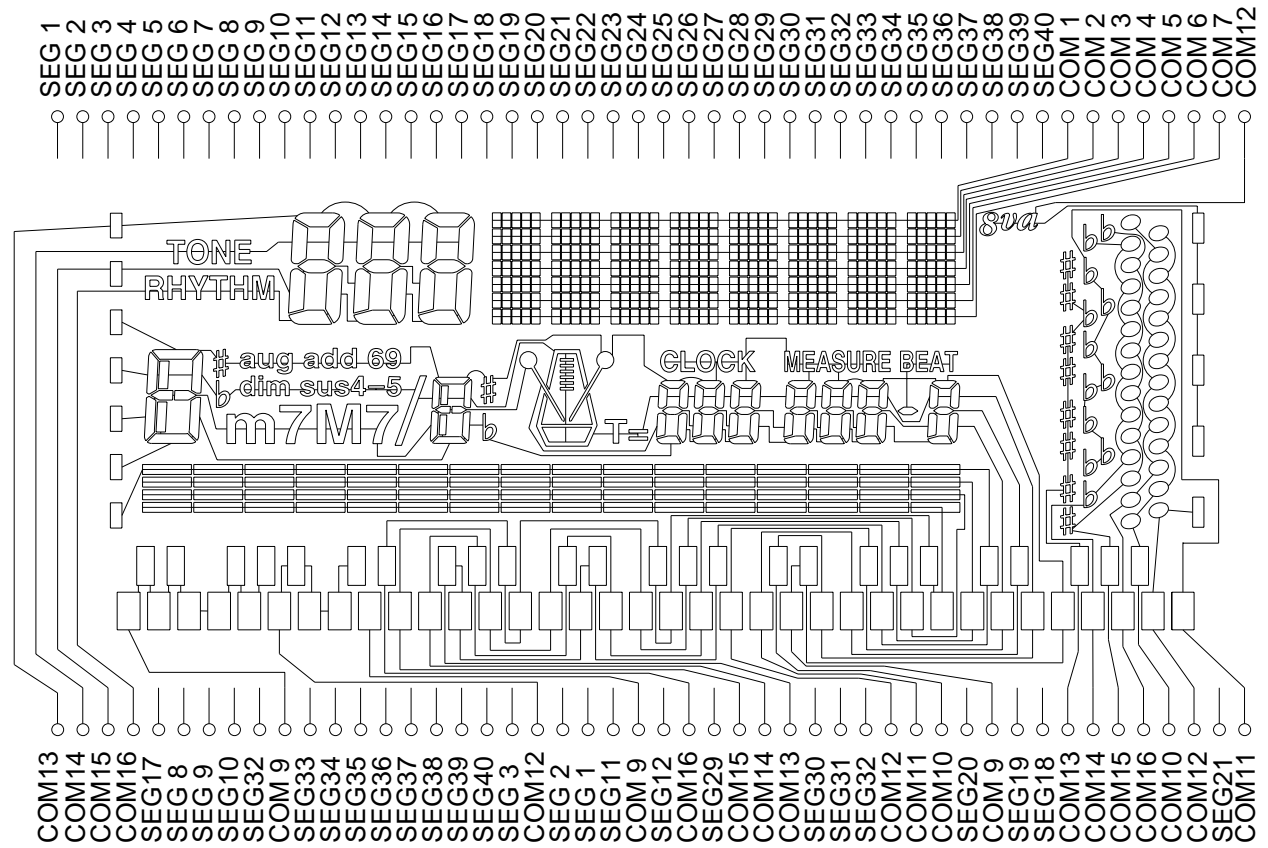


# JCM617T-KY2M

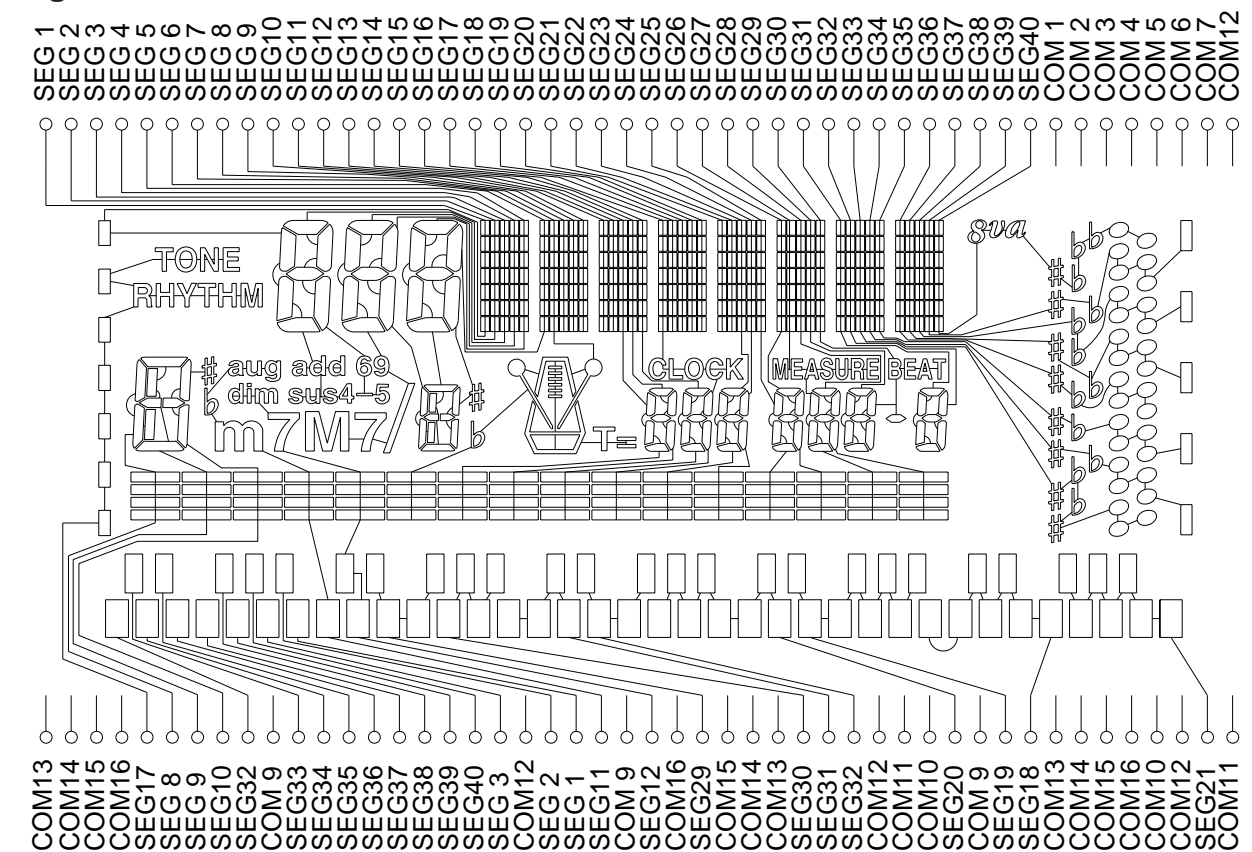


# LCD

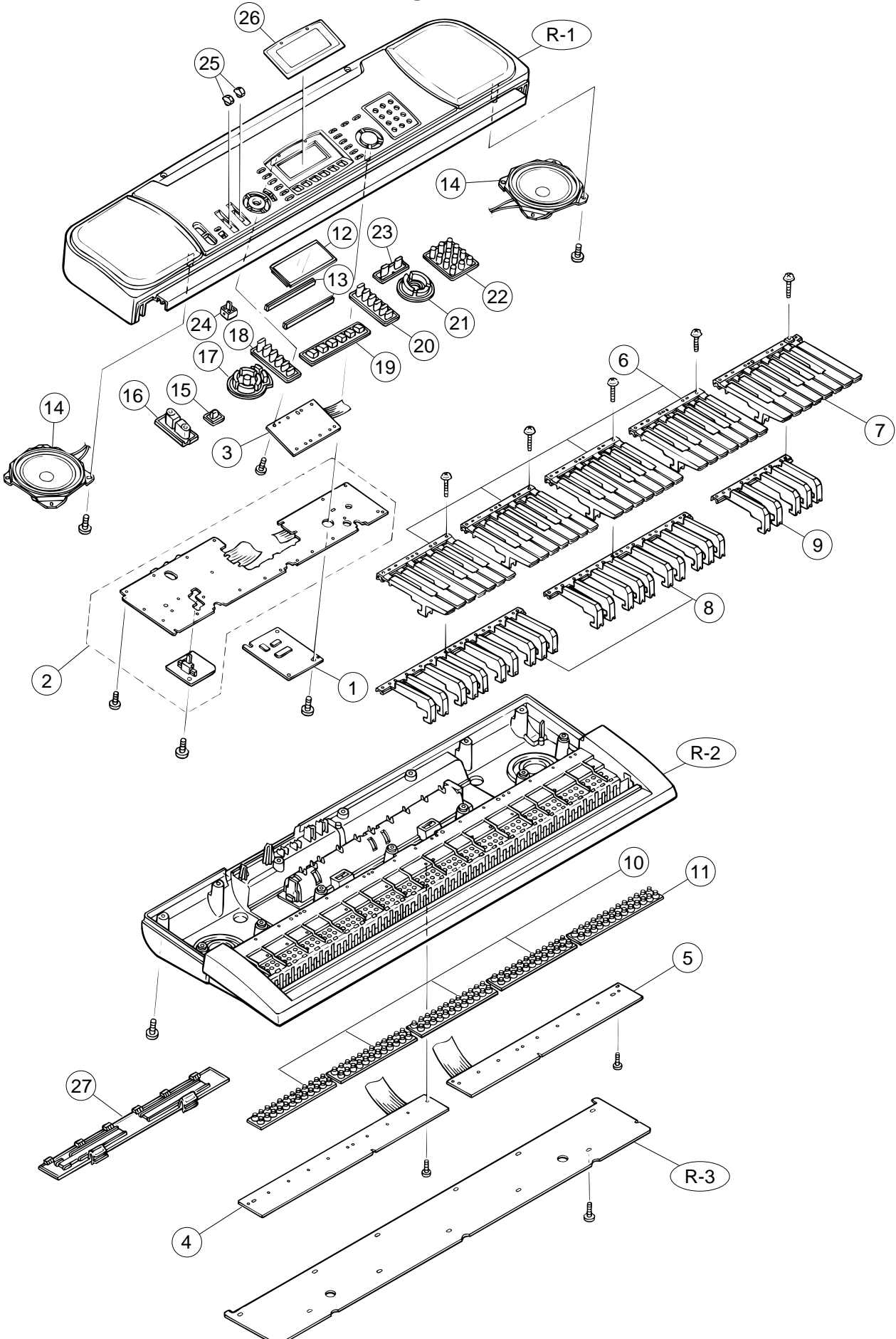
## Common



## Segment



# EXPLODED VIEW



# PARTS LIST

## CTK-601

Notes: This parts list does not include the cosmetic parts, which parts are marked with item No. "R-X" in the exploded view.

Contact our spare parts department if you need these parts for refurbish.

1. Prices and specifications are subject to change without prior notice.
2. As for spare parts order and supply, refer to the "GUIDEBOOK for Spare parts Supply", published separately.
3. The numbers in item column correspond to the same numbers in drawing.

Item	Code No.	Parts Name	Specification	Q	R
<b>Main PCB</b>					
1	6925 8440	Main PCB ass'y, M462-MA1M	M240613*1	1	B
LSI1	2012 4879	LSI, CPU	UPD913GF-3BA(T)	1	A
LSI2	2012 2079	LSI, DSP	HG51B277FB-1	1	A
LSI3	2012 5590	LSI, ROM	UPD23C16000WGX-C51	1	A
LSI4/LSI5	2012 5572	LSI, RAM	TC55257DFL-70L(EL)	2	A
LSI6	2105 4746	LSI, DAC	UPD6379GR-E1	1	A
IC1	2012 1883	IC	RN5VD40AA-TR	1	A
X1	2590 2700	Oscillator, Crystal	XA10412	1	B
X2	2590 2699	Oscillator, Ceramic	EFOB2005E0	1	B
<b>Sub PCB ass'y</b>					
2	6925 8470	Sub PCB ass'y M462-MA2,3M	M140554*1	1	B
IC101	2114 5775	IC, Power amp.	TA8248K	1	A
IC102	2252 1248	IC, Photocoupler	HCPL-261A	1	B
Q101, Q103	2250 0742	Transistor	2SA1703S,T-AN-T	2	A
	2252 0784	Transistor	2SC1740SQ,R-TP-T	7	A
Q105	2250 1578	Transistor	2SC4483S,T-AN	1	A
D101/D102	2390 3018	Diode	1T2	2	B
	2390 1344	Diode	1SS133T-77-T	40	B
D106	2360 1085	Diode, Zener	HZS6B1LTD-T	1	B
D107	2360 1946	Diode, Zener	MTZJ5.6CT-77-T	1	A
D190	2360 1673	Diode, Zener	MTZJ5.6AT-77-T	1	A
LED101	2320 3146	LED	TLR124	1	B
J101	3501 7049	Jack, Power	HEC2305-01-330	1	A
J102	3612 0665	Jack, Phone	YKB21-5006	1	B
J103	3612 0789	Jack	YKB21-5010	1	B
J104	3501 4816	Jack, DIN	YKF51-5051	1	B
VR101	2765 2178	Volume	EWASC2C95B23	1	A
<b>Display PCB</b>					
3	6925 8420	Display PCB ass'y M462-LCD1M	M240609*1	1	B
LSI401	2012 5569	LSI, LCD driver	SED1278F0A	1	A
VR401	2775 3286	Potentionmeter, Chip	EVM3SSX50B53	1	A
<b>Keyboard PCBs</b>					
4	6924 2580	PCB ass'y M617T-KY1M	M140211*5	1	B
D501~D564	2301 0101	Diode	1S2473-T-77-T	64	B
5	6925 8490	PCB ass'y M617T-KY2M	M140212*9	1	B
D565~D622	2301 0101	Diode	1S2473-T-77-T	58	B
<b>Keyboard unit</b>					
6	6922 2720	White key set, LT-CB	M312118*1	4	A
7	6922 2730	White key set, LT-CS	M312118*2	1	A
8	6922 2740	Black key set 10P	M111726-1	2	A
9	6922 2750	Black key set 5P	M111726-2	1	A
10	6922 2761	Key contact rubber LT-CB	M211704A-1	4	A
11	6922 2771	Key contact rubber LT-CS	M211705A-1	1	A
<b>Panel unit</b>					
12	3335 6551	LCD	LD-B10088A	1	B
13	6925 8590	Interconnector	M440426-2	2	B
14	3831 0833	Speaker	S12J49A	2	B
15	6923 4980	Rubber button 711A	M312122-2	1	B
16	6925 8610	Rubber button 462A	M240543-1	1	B
17	6925 8620	Rubber button 462B	M140516-1	1	B
18	6925 8630	Rubber button 462C	M240544-1	1	B
19	6925 8640	Rubber button 462C	M240544-2	1	B
20	6925 8650	Rubber button 462D	M240545-1	1	B
21	6925 8660	Rubber button 462E	M240546-1	1	B
22	6925 8670	Rubber button 462F	M240547-1	1	B

Item	Code No.	Parts Name	Specification	Q	R
23	6925 8680	Rubber button 462G	M240548-1	1	B
24	6913 6410	Slide contact 12D	CSB-12D	1	B
25	6921 5030	Slide knob	M311859-1	2	B
26	6925 8580	Display plate	M240568-1	1	C
27	6906 8456	Battery cover	M311164F*12	1	B
<b>Accessory</b>					
	6925 8690	Music stand	M340629*1	1	B

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